

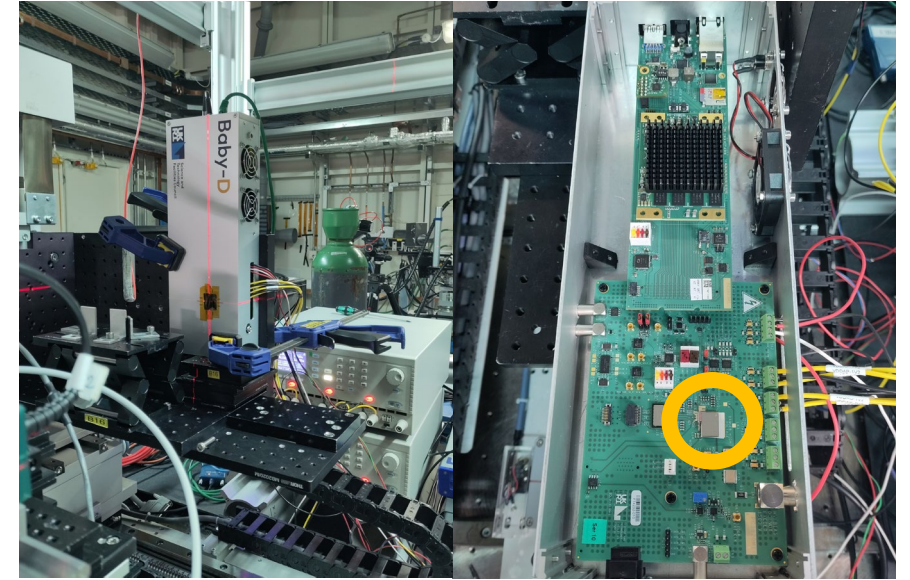
DynamiX

**A prototype high-framerate, high-dynamic-range
hard X-ray detector for 4th generation synchrotrons**

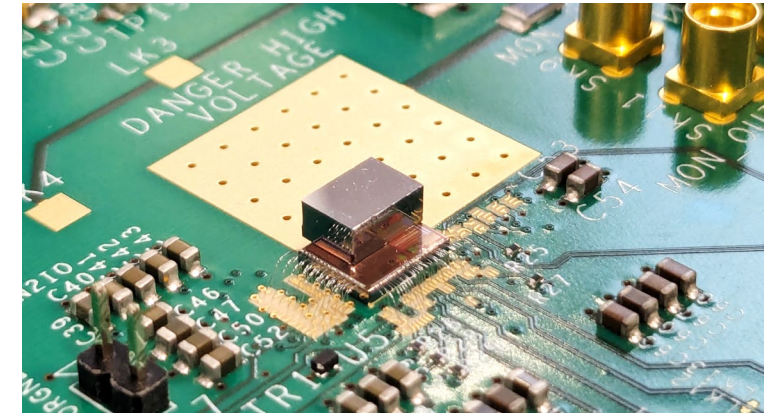
Simon Knowles (STFC UKRI)
On behalf of the XIDyn Collaboration
iWoRiD 2025

DynamiX Overview

- Charge-integrating hybrid pixellated detector system prototype
- Aims to meet challenges of synchrotron upgrades
 - Higher fluxes → new ASIC architecture
 - Higher energies → CZT sensor material
- 16 x 16 pixels on 110μm pitch
- 534kHz (adjustable) framerate to match Diamond orbit
- Readout at 14Gbps



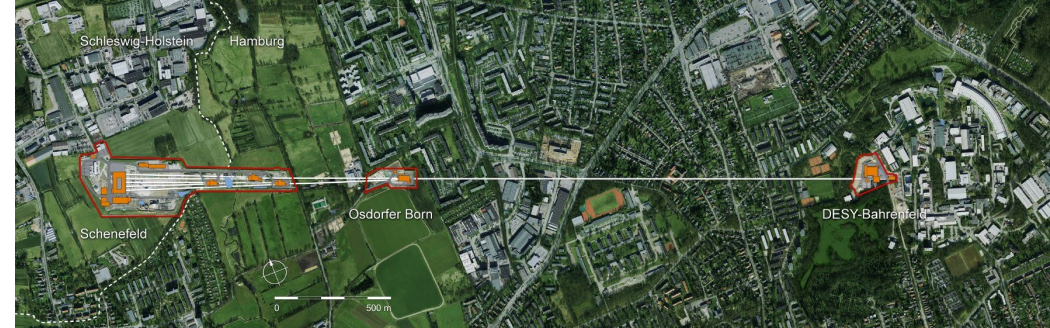
Aligning DynamiX at Diamond Light Source (DLS)



DynamiX bonded to 2mm Redlen HF-CdZnTe

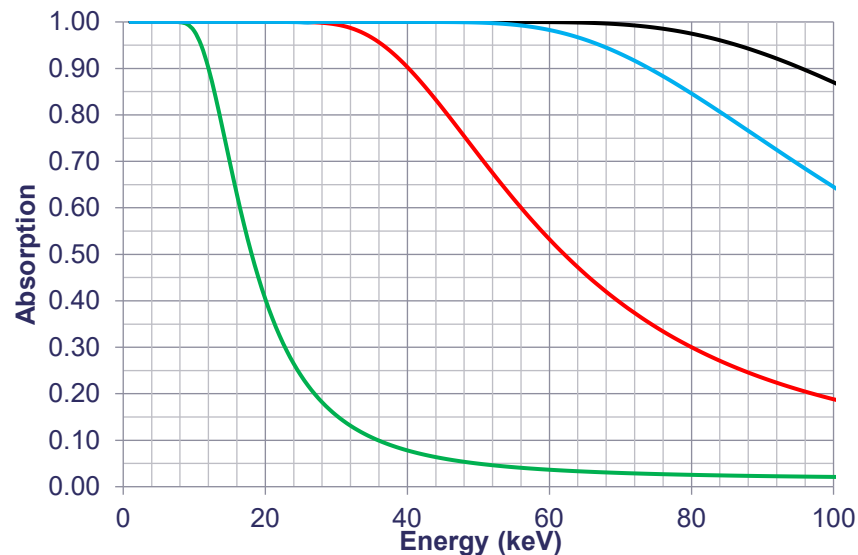
Content

- Motivations
- Chip architecture
- Results from testing at DLS B16
- Summary

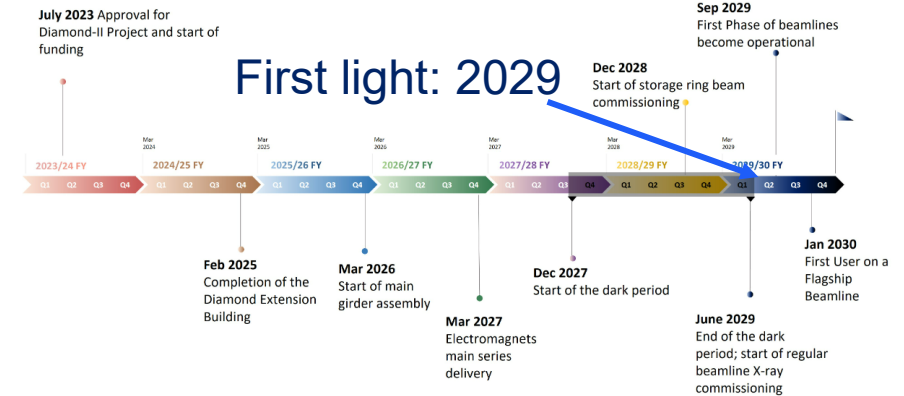
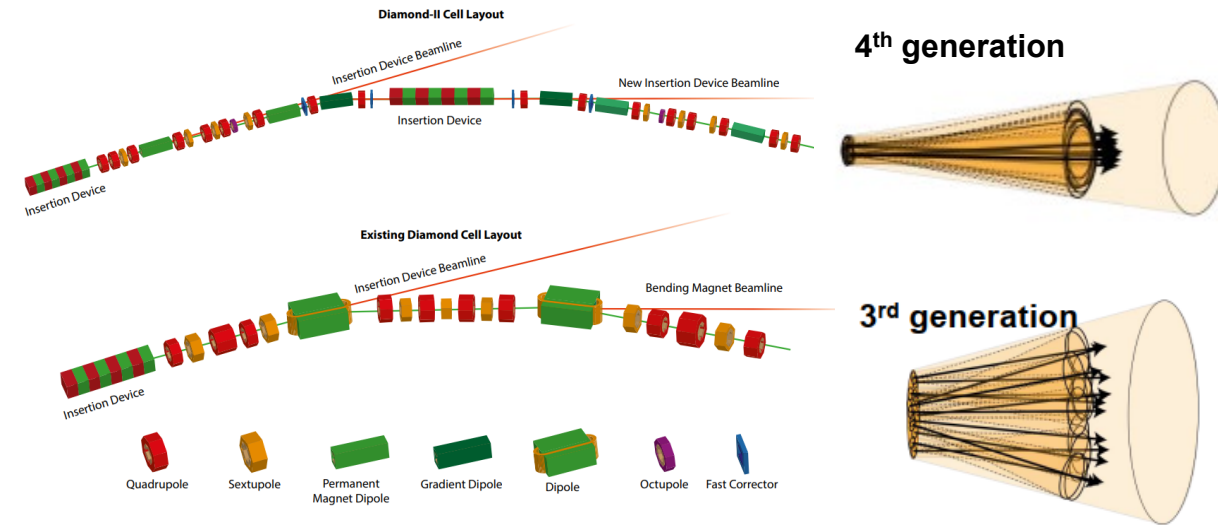


Motivation

- Diamond II upgrade
- Up to 10^{11-12} ph/mm²/s
- Many beamlines >20keV
- Mpixels for 2029



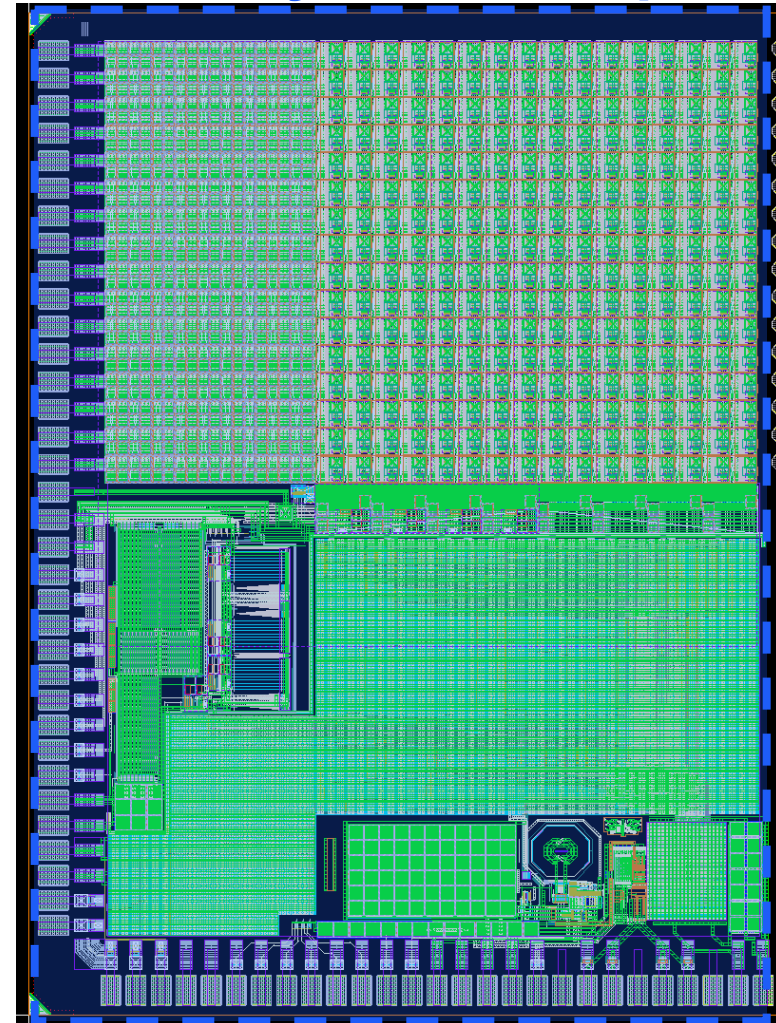
— 0.5mm Si
— 2mm CZT
— 1mm GaAs
— 1mm CdTe



DynamiX Spec

Parameter	DynamiX
Flux	$>10^9$ ph/s/pixel
Pixel Size	110 μm
Frame Rate	534 kHz (Synchrotron determined)
Dynamic Range	$>10^{11}$ ph/mm ² /s
Energy	25keV typical
Noise Level	<single photon

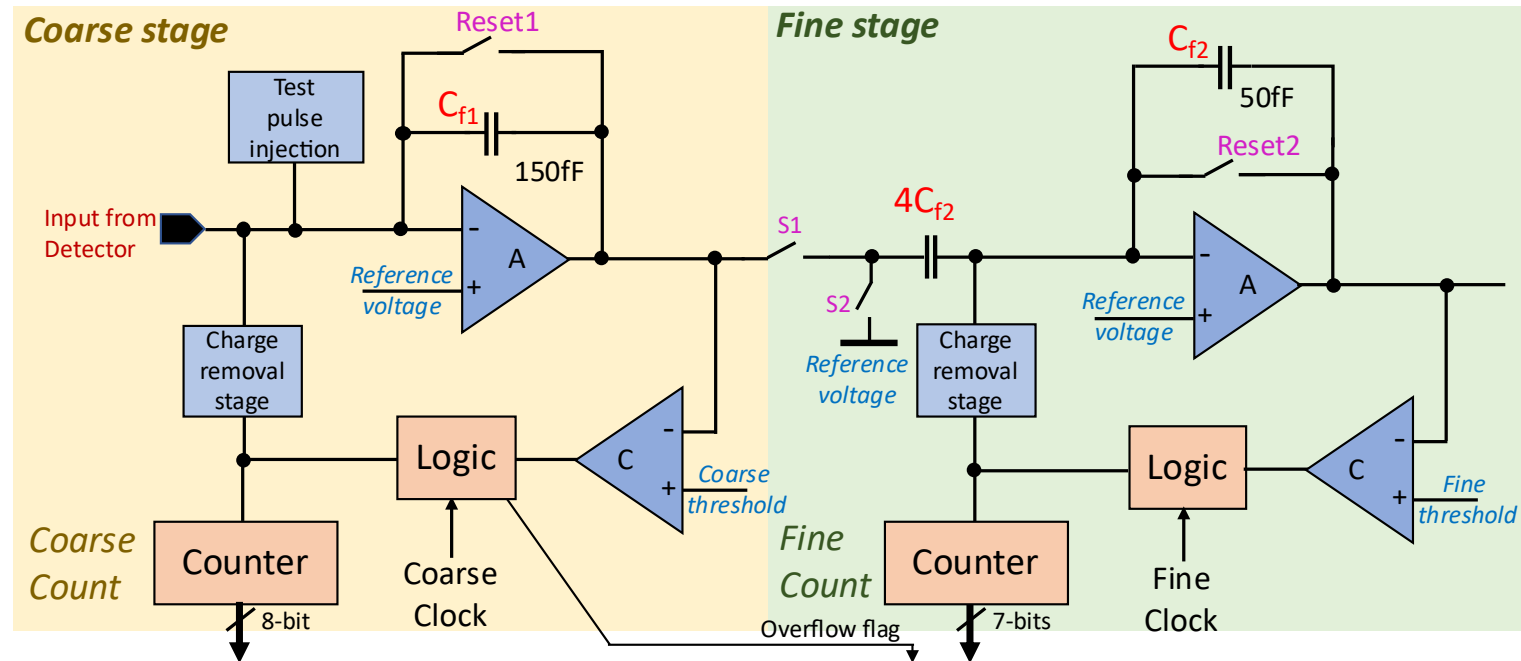
The DynamiX Chip



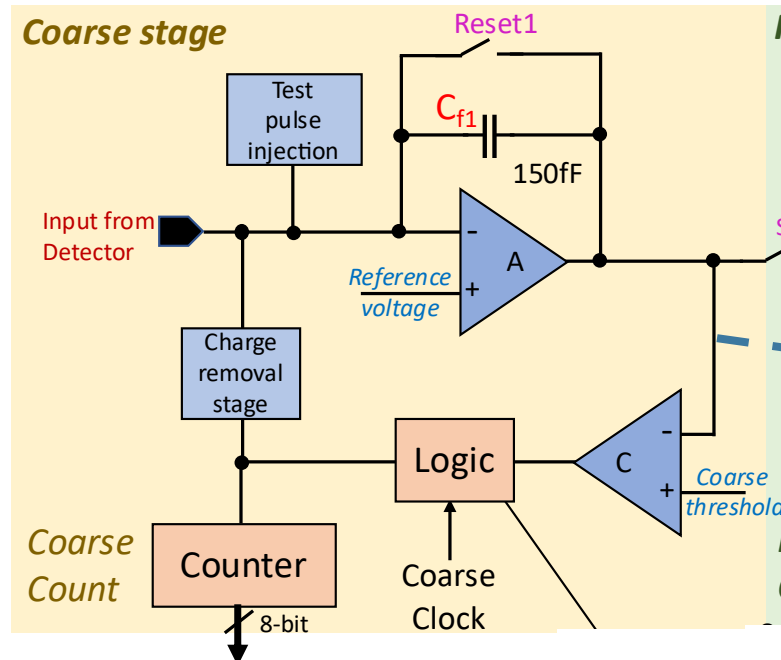
Chip Architecture

Incremental digital integration (per pixel):

- 2 stage charge cancellation circuit
- Signal integrated in coarse stage cancelled and remainder passed to fine stage
- Coarse and Fine counters output



Coarse Stage (<6400 ph/pix/frame @ 30keV)



Signal

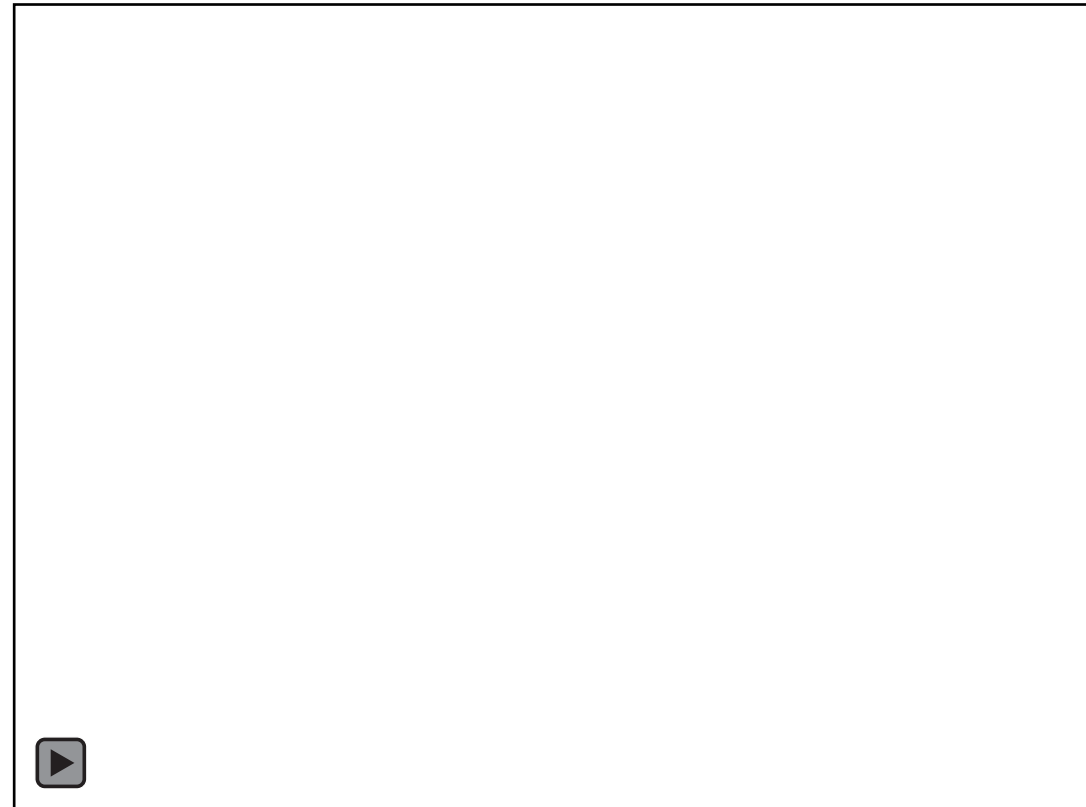
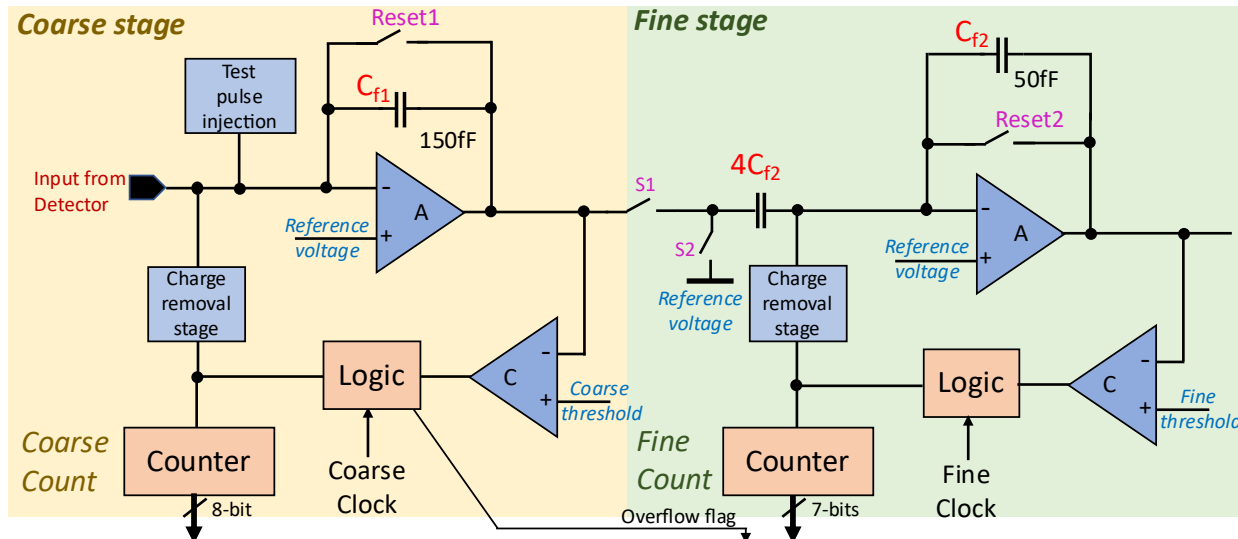
Coarse threshold

Coarse counts

Transfer and Fine Stage (<25 ph/pix/frame @ 30keV)

NEW

NEW

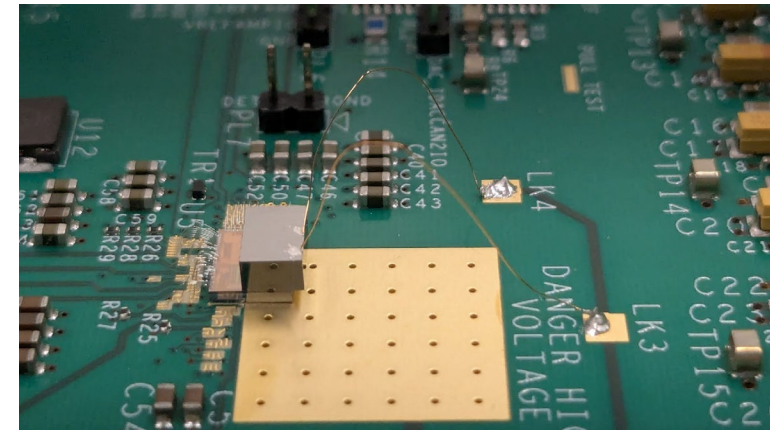
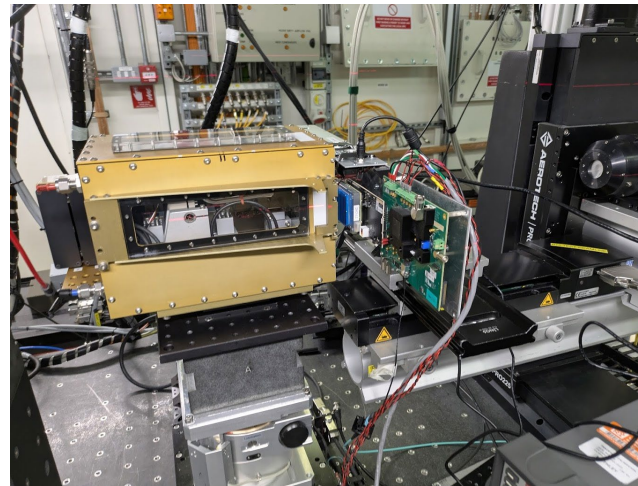
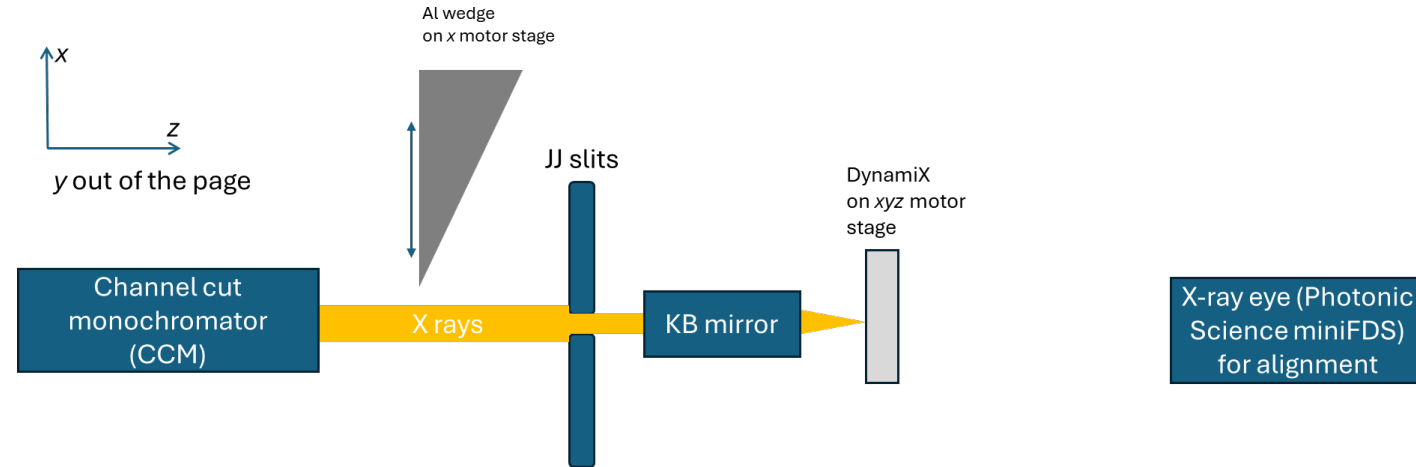


Diamond

Results from beam tests at DLS B16

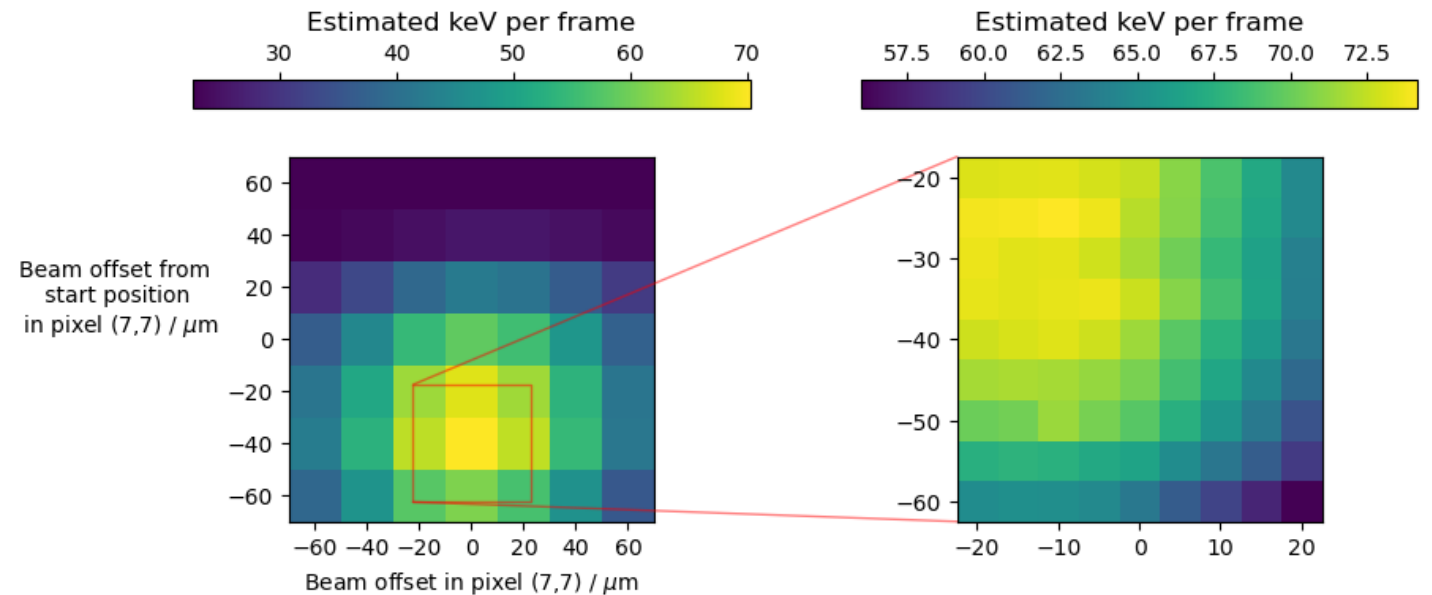
Setup at DLS B16 Test Beamline

- Focusing used to access sub-pixel-sized beam
- Beam energies of 20,30,40 and 50keV
- Sensor biased to -1000V
- Later switched to DCM to access higher fluxes at 20keV



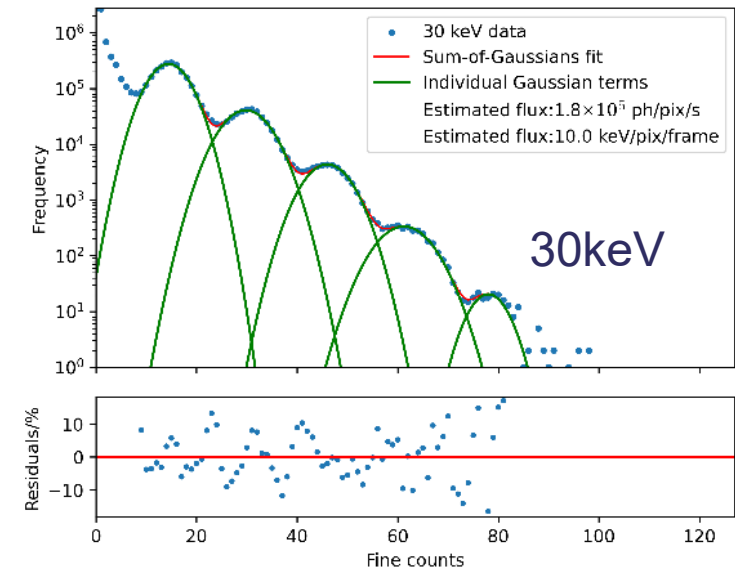
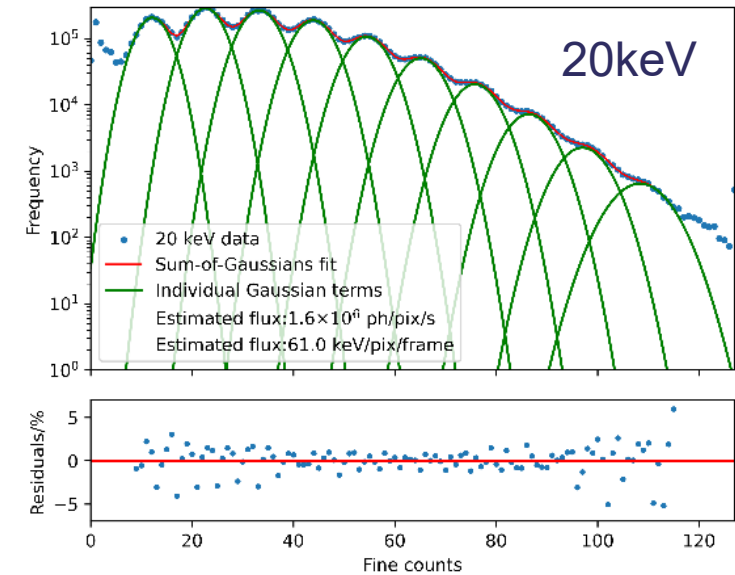
Charge sharing – raster scanning beam within pixel (20keV, $\sim 20\mu\text{m}$ beam)

- Measured signal in pixel strongly depends on beam alignment to centre of pixel
- Evidence of charge sharing to neighbouring pixels in CZT
- Destroys ability to resolve single photon peaks \rightarrow challenging to calibrate without pencil beam centred in-pixel



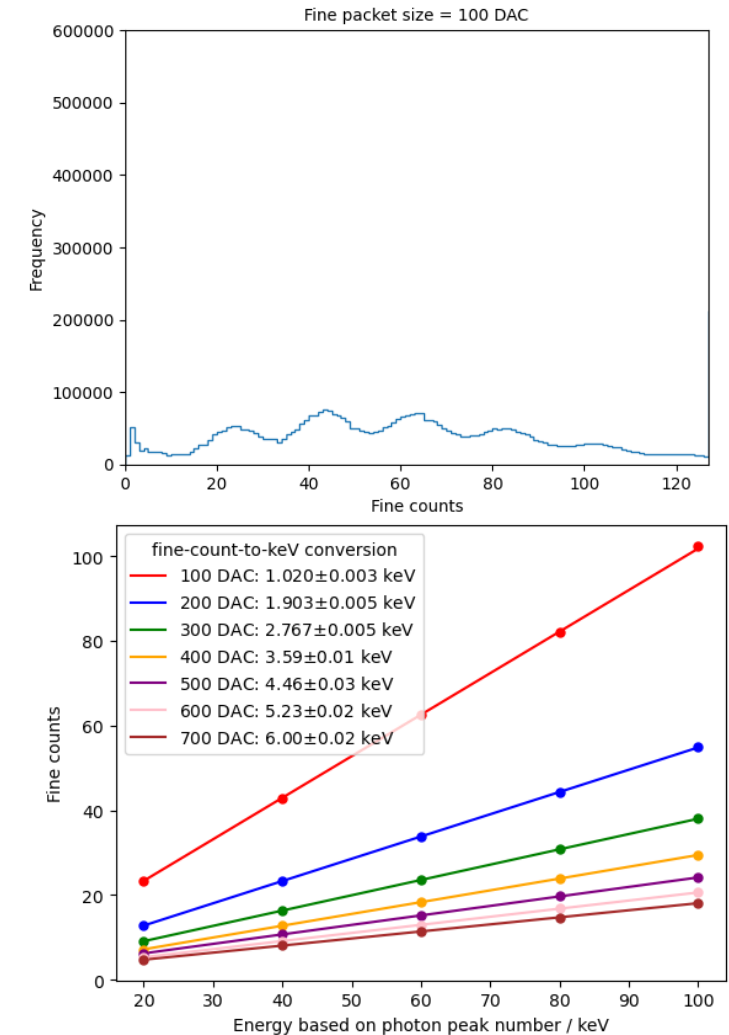
Single photon peaks

- Able to resolve single-photon peaks (up to 10 single-photon peaks for 20keV beam)
- Width $\sigma \approx 5.7\text{keV}$ ($\approx 1200\text{ e}^-$ RMS)
- Photodiode-measured fluxes:
 - 20keV: $1.58 \times 10^6\text{ ph/pix/s}$
 - 30keV: $2.80 \times 10^5\text{ ph/pix/s}$



Single photon peaks – changing fine packet size

- Measurements taken at 20keV, flux $\approx 1.6 \times 10^6$ ph/s/pix
- Can change digitisation resolution at expense of dynamic range
- Smaller packets \rightarrow more packets to remove same charge
- Better quality fit with smaller packets \rightarrow easier to estimate noise
- Settled on packet size 100 DAC as this looks like 1 fine count \approx 1 keV

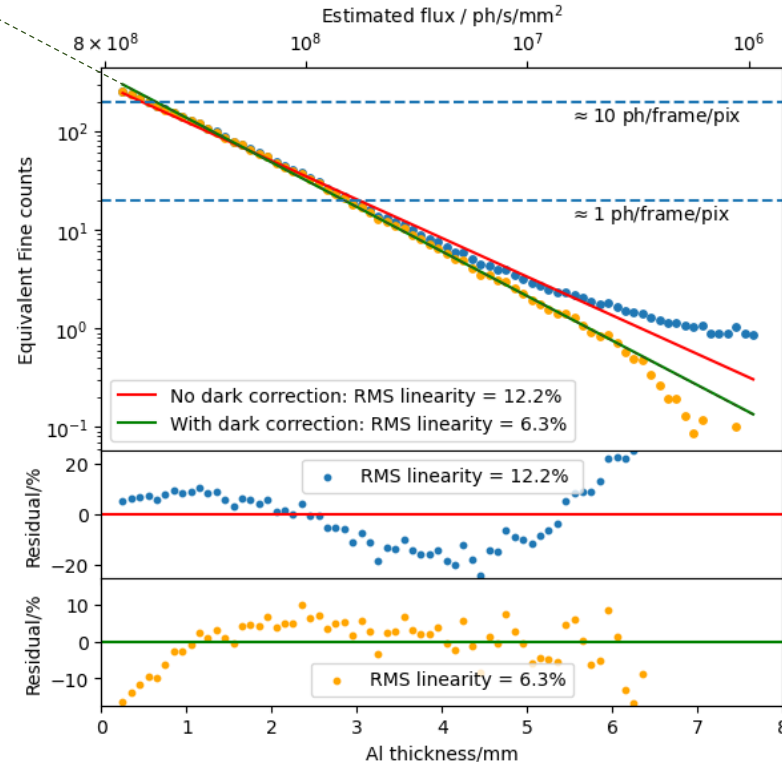


Linearity with increasing flux

Limit of flux from X ray set:
 $\approx 3 \times 10^{10}$ ph/s/mm² equiv. @ 20keV

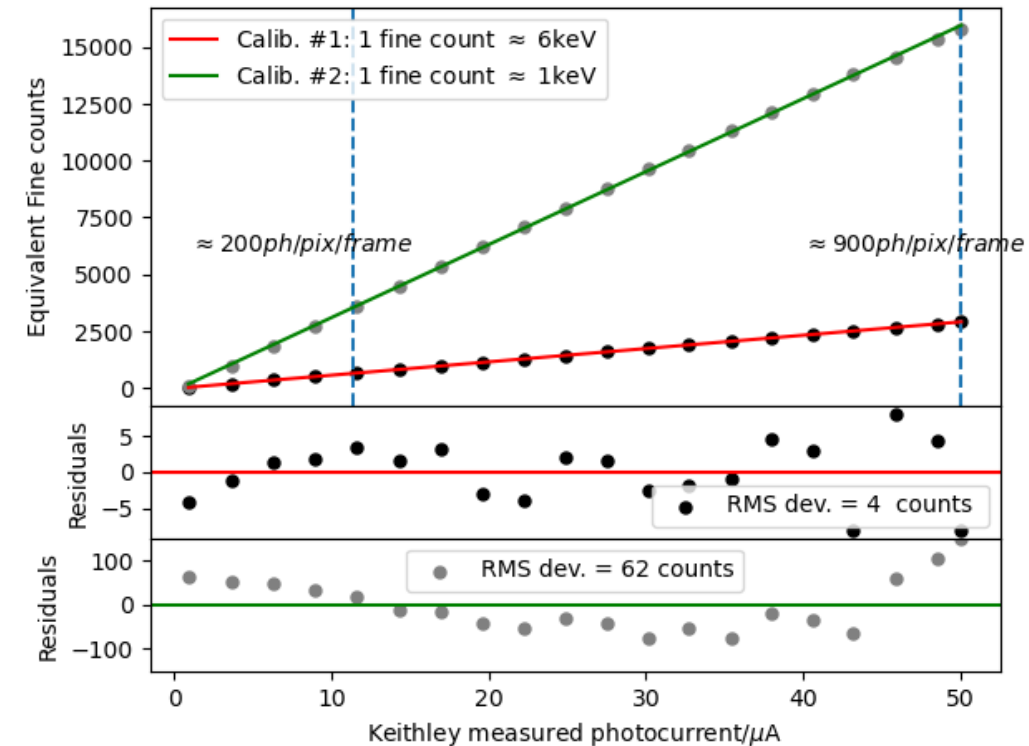
20keV beam, moving Al wedge into beam

Design spec:
 $\approx 4 \times 10^{11}$ ph/s/mm²
 @20keV



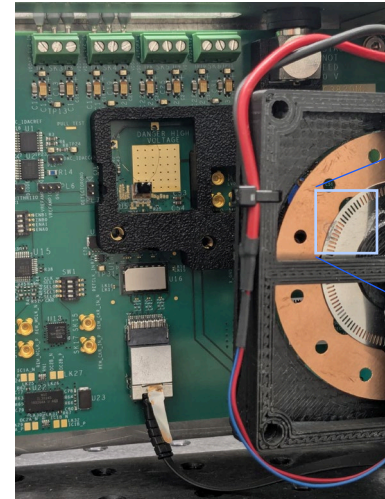
Limited by flux available
 at the beamline, not the
 DynamiX detector

160kV W X-ray set (polychromatic)

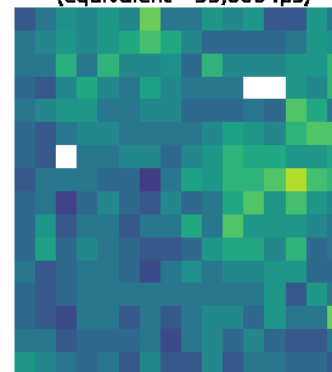


Whole array measurements

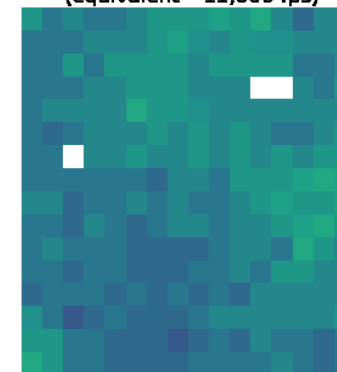
- Imaged a spinning slitted disc (copper and steel) at 533kHz
- 100kV tube voltage, 30mA tube current
- Frames flat-field corrected
- Averaged over windows of 10, 50 and 100 frames



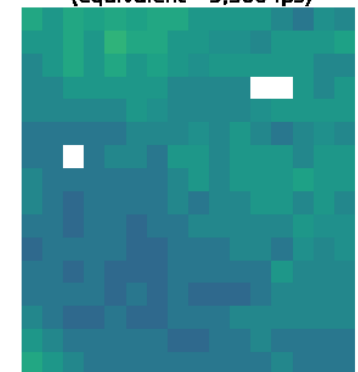
10 raw frames per gif frame
(equivalent ~53,000 fps)



50 raw frames per gif frame
(equivalent ~11,000 fps)

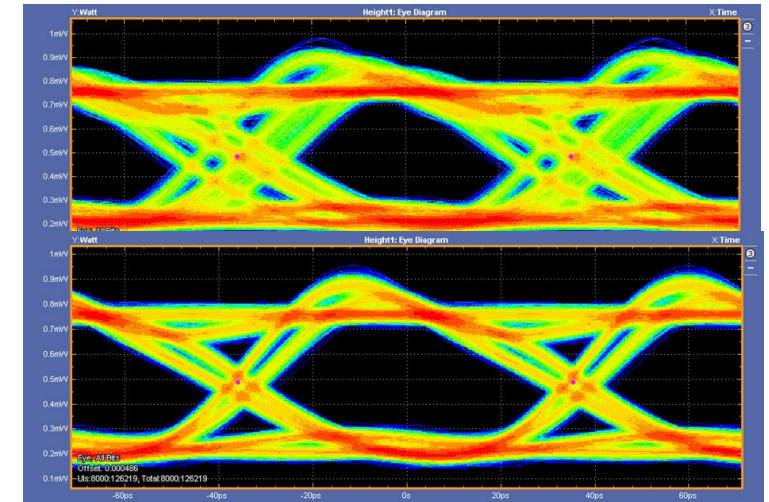


100 raw frames per gif frame
(equivalent ~5,300 fps)

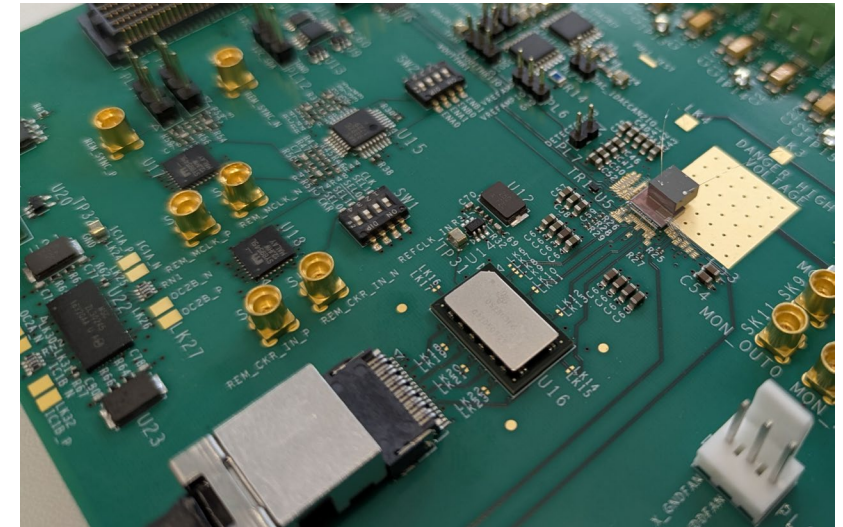


14 Gbps Serialiser Results

- Aurora encoded 66b64b CML
- 14 Gbps working and continuously writing to disc at 534 kfps
- Data passes through retimer IC → allows inspection of eye quality
- Conversion to optical via Samtec FireFly close to ASIC
- Receiver and frame built in FPGA with 100G UDP out



Eye diagrams without and with retiming



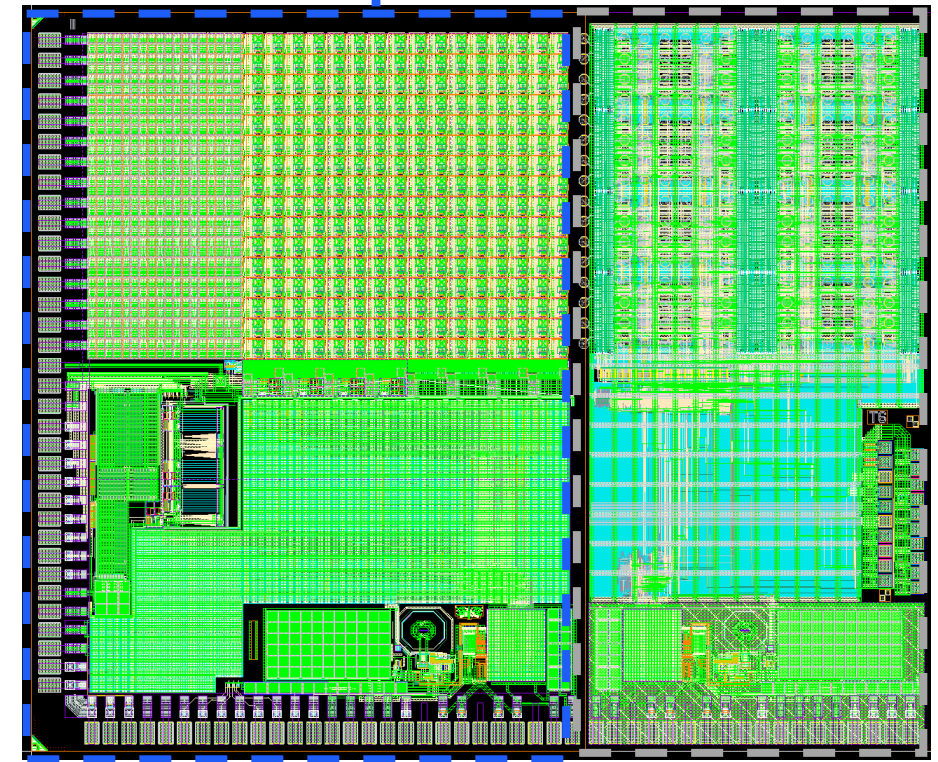
Samtec FireFly and retimer IC next to Dynamix ASIC

Towards the full-scale ASIC, XIDyn

- 192 x 144 pixel ASIC, combining XIDER and DynamiX designs
- Improvements to pixel design, including:
 - Increased trim range for calibration of thresholds & packet sizes
 - Switch to disconnect front end from pixel during transfer to prevent saturation
- Submission planned for autumn this year, with wafers expected back in spring

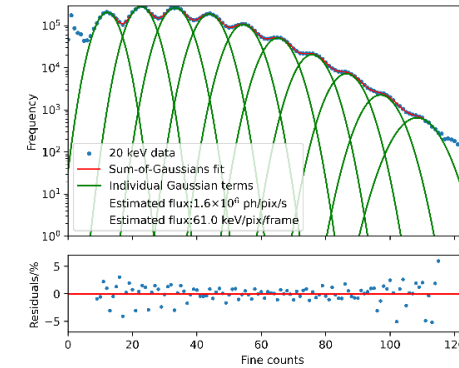
DynamiX
Chip

XIDER
Chip

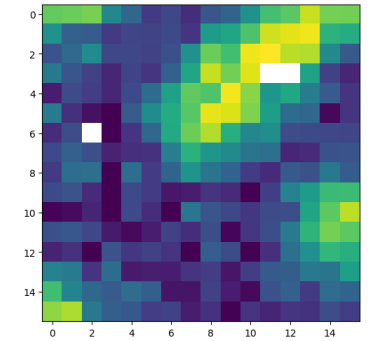


Summary

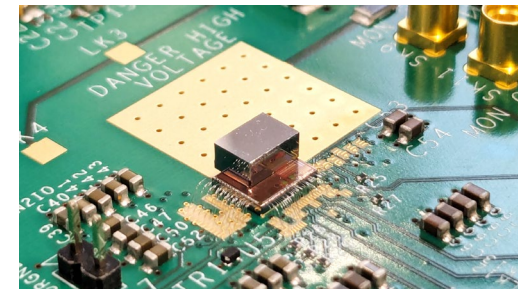
- Continuous full-frame readout at 534 kHz
- Able to resolve single photons with width $\sigma \approx 5.7\text{keV}$
- Measurements taken in range $<1\text{ph/pix/frame} \rightarrow \approx 900\text{ ph/pix/frame}$
- Submission of full ASIC in autumn, tileable on 3 sides
- Upcoming beamtimes at Diamond & ESRF to go to higher fluxes



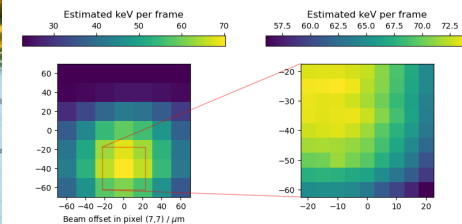
Single-photon peaks @ 20 keV



Still from imaging spinning slitted disc at 534kfps



The DynamiX ASIC with Redlen HF-CZT

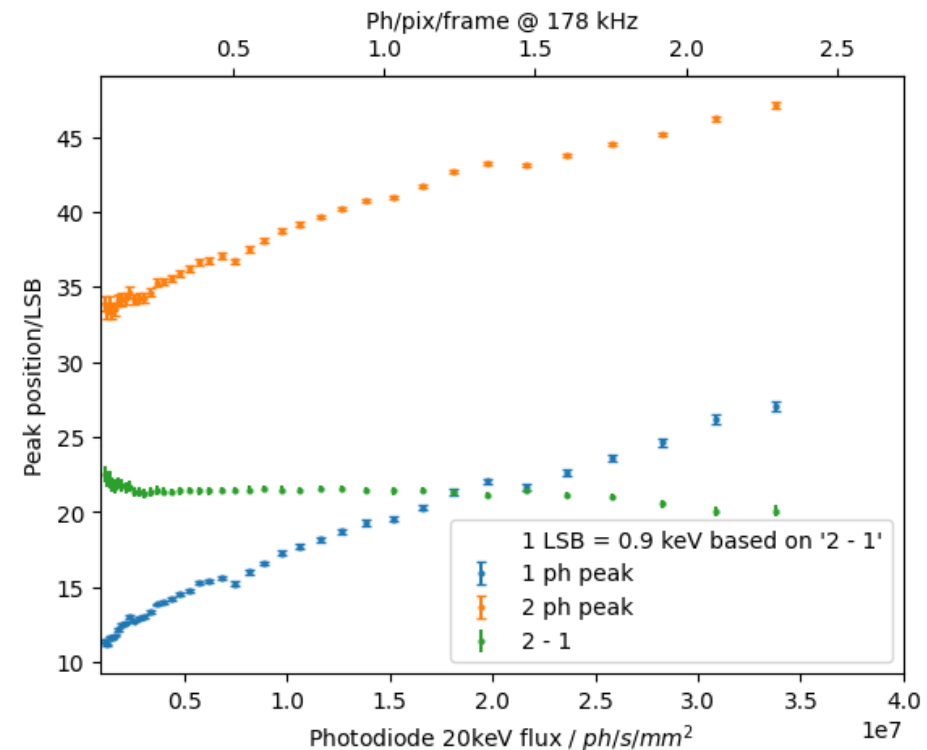
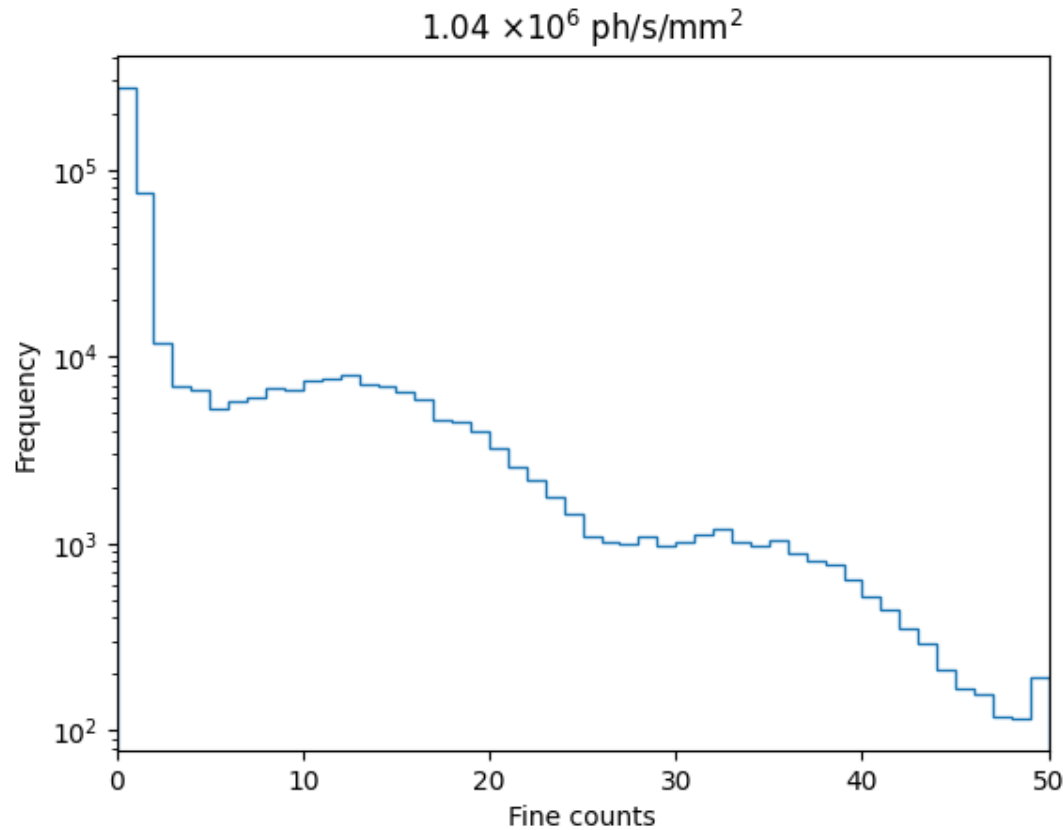


Rasting scanning beam within pixel

Additional Slides

Leakage current shift (FL = 3 Diamond turns)

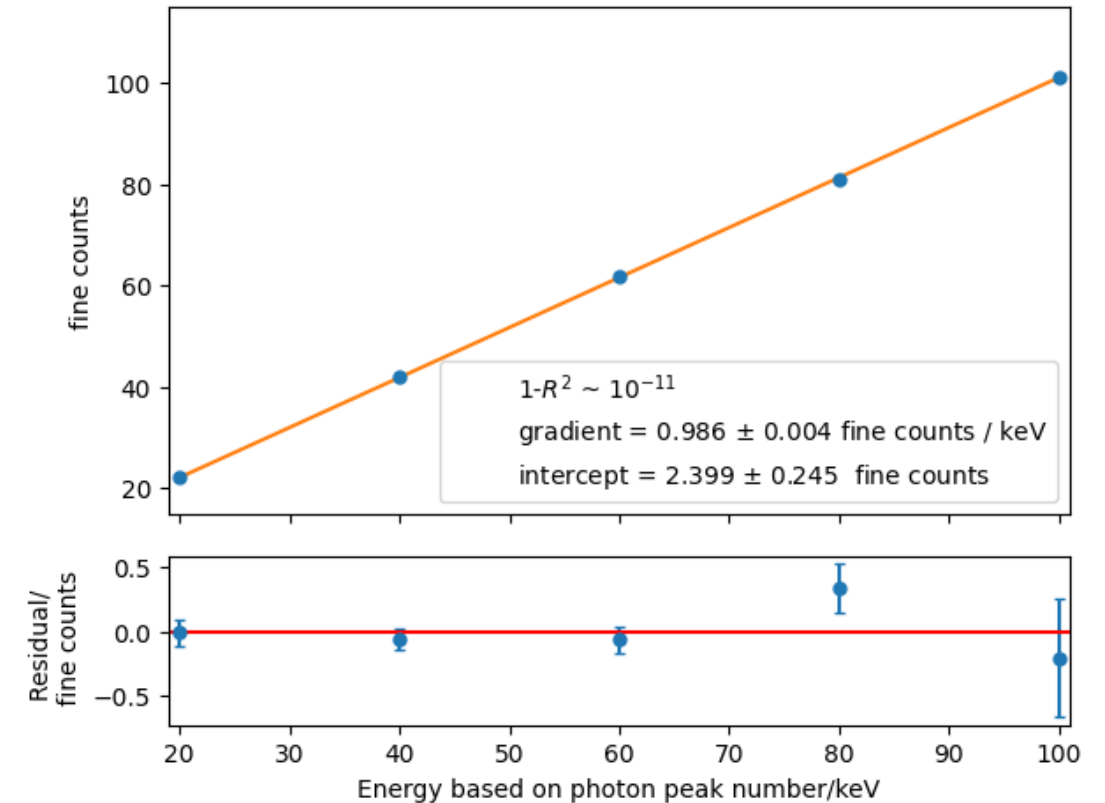
- 1 and 2 photon peaks move to higher channel numbers with increasing flux
- With FL = 1 turn, beam $50\text{ }\mu\text{m} \times 50\text{ }\mu\text{m}$, excess leakage current density in this flux range $\approx 41\text{ nA/mm}^2$
- This effect has been seen in Redlen HF CZT before, e.g. Cline and XIDER



Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

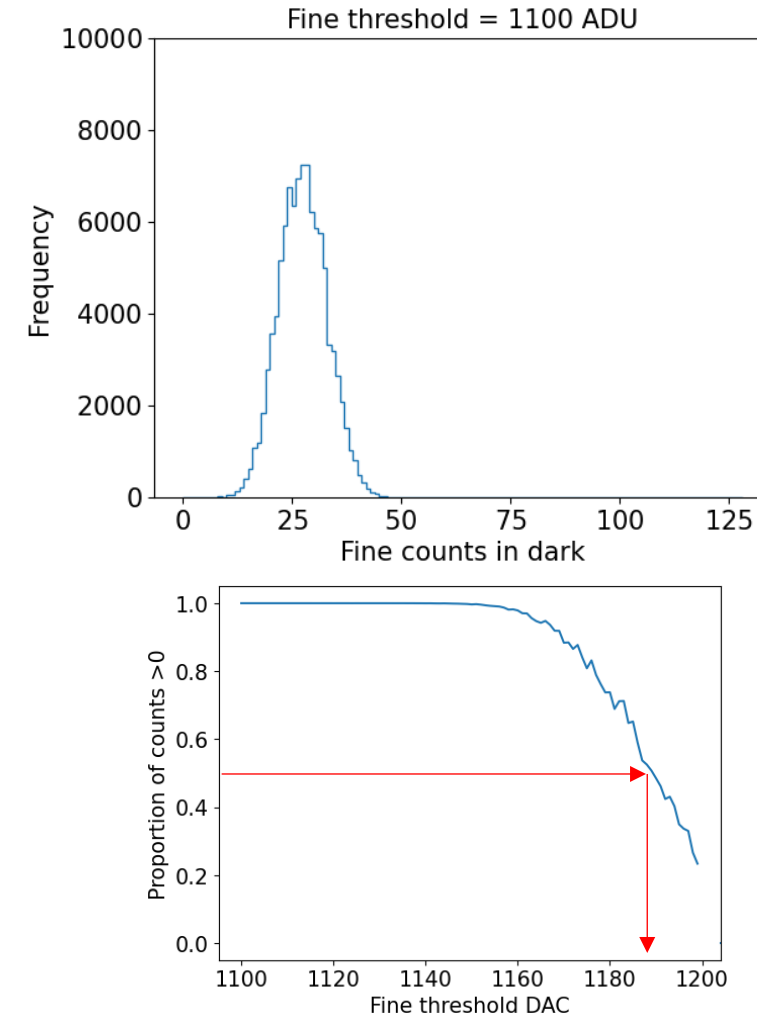
1. Determine ADU \rightarrow keV conversion from photon peak spacing



Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

1. Determine ADU \rightarrow keV conversion from photon peak spacing
2. Change fine threshold to move noise peak to 0
 - Find threshold such that 50% counts are >0



Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

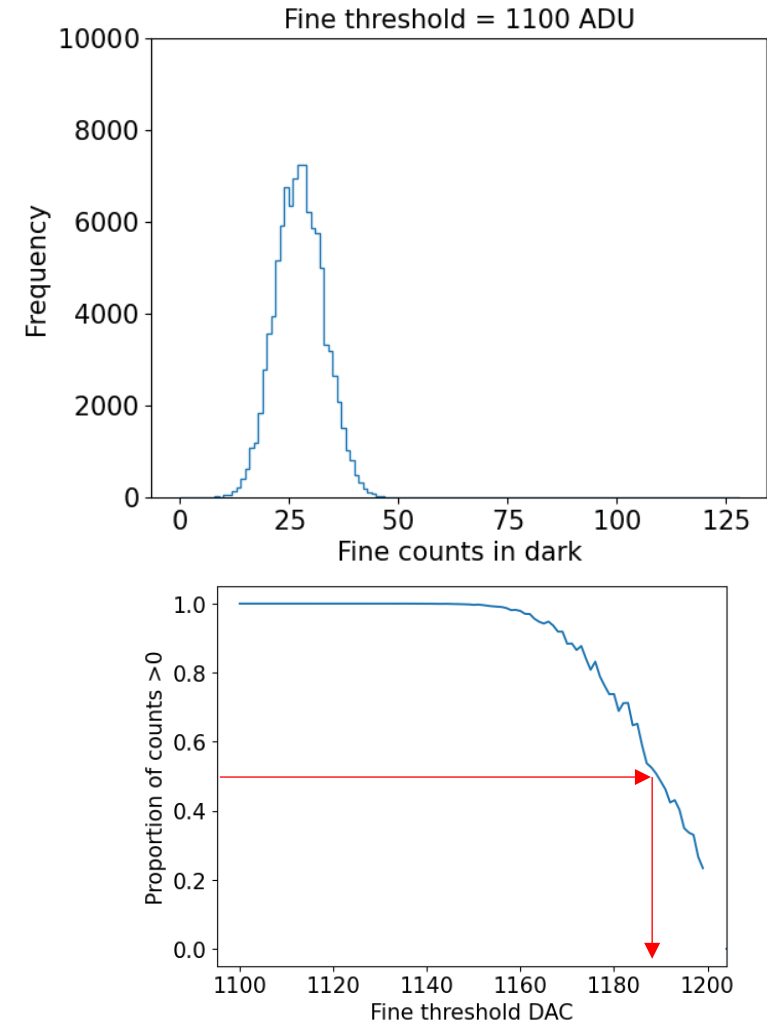
1. Determine ADU \rightarrow keV conversion from photon peak spacing

2. Change fine threshold to move noise peak to 0

- Find threshold such that 50% counts are >0

Can also estimate noise:

- Look at σ of dark counts where distribution is well away from 0
- $\sigma \approx 5.50 \text{ LSB} \approx 5.57 \text{ keV} \approx 1210 \text{ e}^-$
- 150fF fine stage feedback, includes leakage + sensor capacitance



Calibrating from single photon peaks

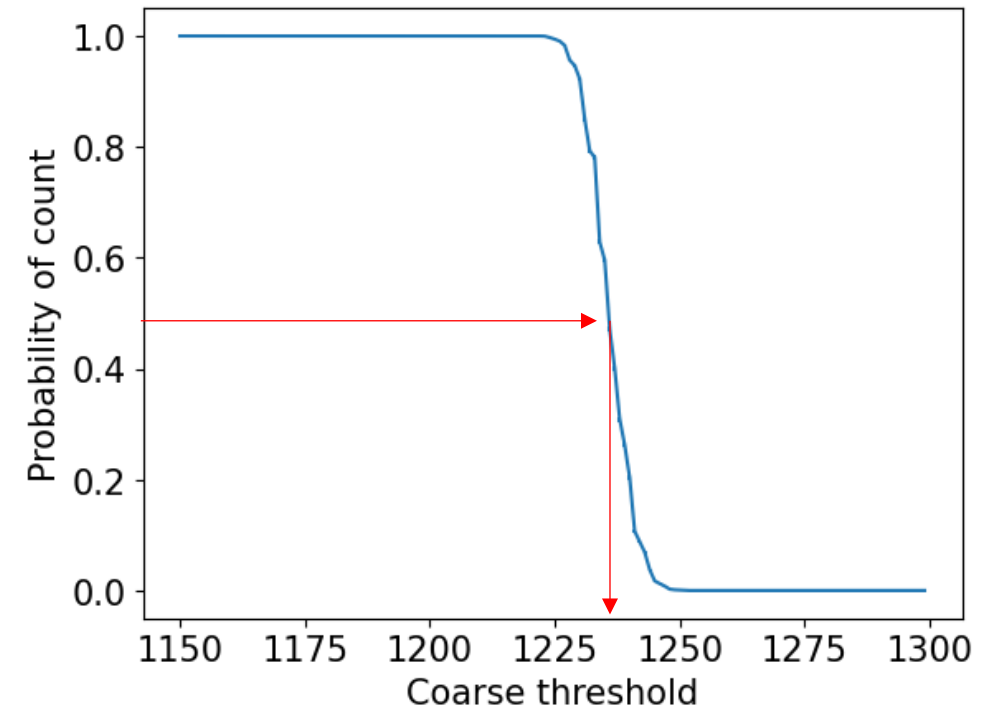
(aim: 1 coarse = 64 fine)

1. Determine ADU \rightarrow keV conversion from photon peak spacing
2. Change fine threshold to move noise peak to 0
 - Find threshold such that 50% counts are >0
3. Find test pulse setting to give 64 fine counts (≈ 65 keV)

Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

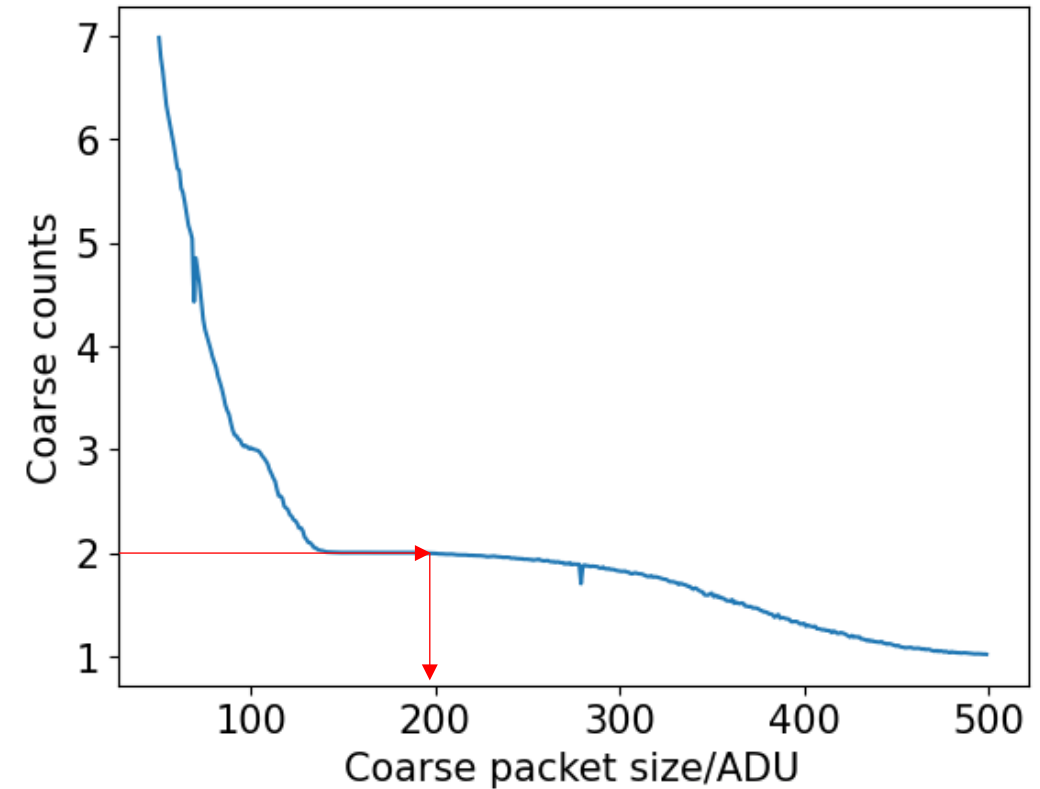
1. Determine ADU \rightarrow keV conversion from photon peak spacing
2. Change fine threshold to move noise peak to 0
 - Find threshold such that 50% counts are >0
3. Find test pulse setting to give 64 fine counts (≈ 65 keV)
4. Sweep coarse threshold while injecting 64 fine counts
 - Find threshold such that 50% chance of triggering
 - Set threshold slightly higher to avoid cancelling below baseline



Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

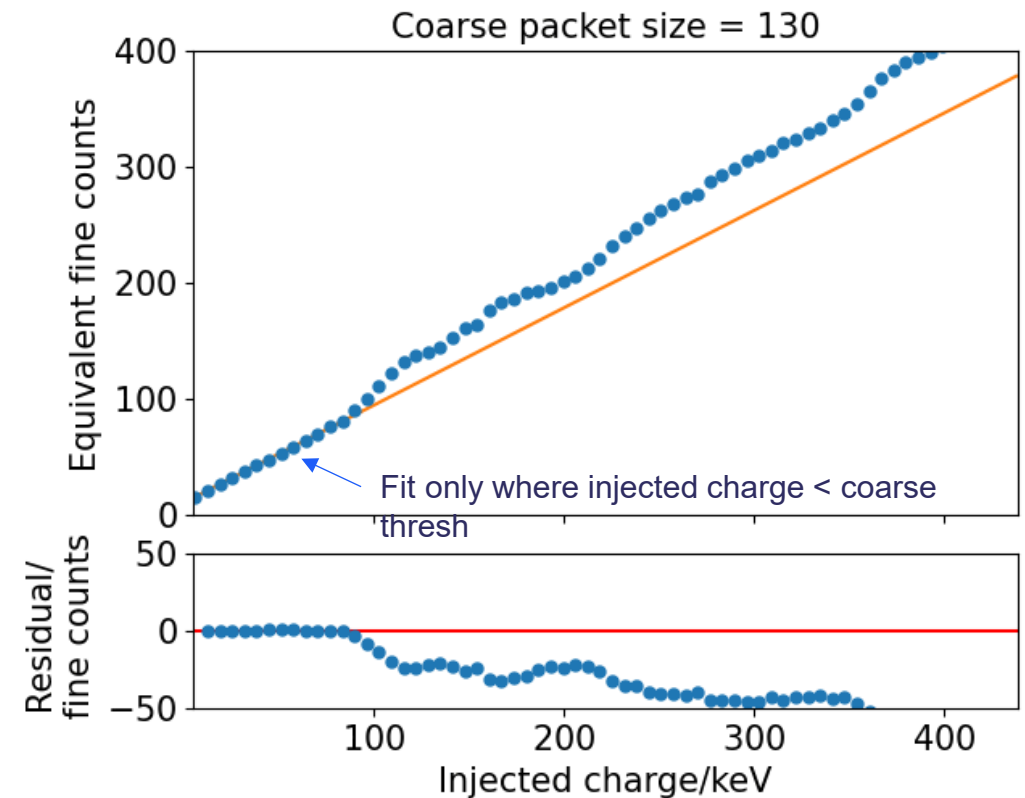
1. Determine ADU \rightarrow keV conversion from photon peak spacing
2. Change fine threshold to move noise peak to 0
 - Find threshold such that 50% counts are >0
3. Find test pulse setting to give 64 fine counts (≈ 65 keV)
4. Sweep coarse threshold while injecting 64 fine counts
 - Find threshold such that 50% chance of triggering
 - Set threshold slightly higher to avoid cancelling below baseline
5. Inject 3 coarse counts ($= 3 \times 64$ fine counts) and sweep coarse packet size
 - Find maximum packet corresponding to 2 coarse counts



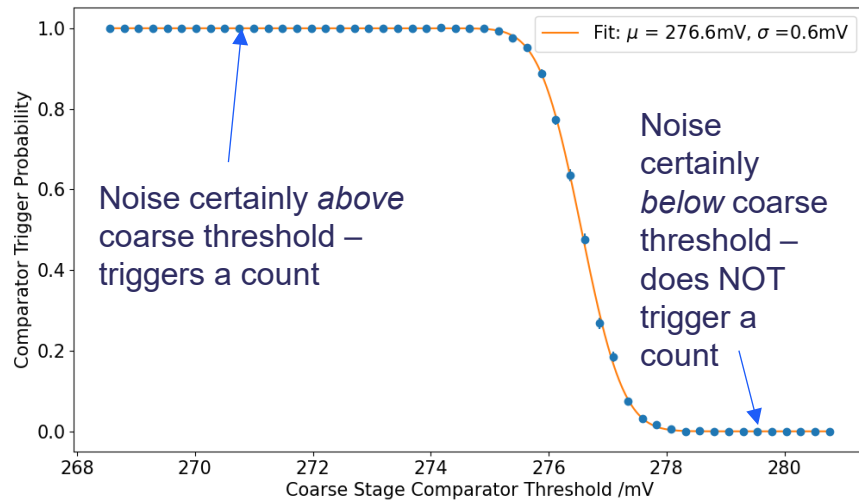
Calibrating from single photon peaks

(aim: 1 coarse = 64 fine)

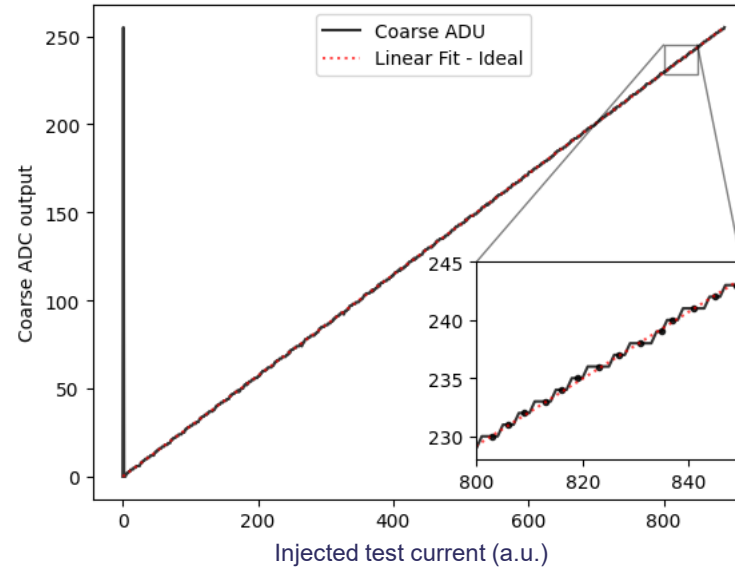
1. Determine ADU \rightarrow keV conversion from photon peak spacing
2. Change fine threshold to move noise peak to 0
 - Find threshold such that 50% counts are >0
3. Find test pulse setting to give 64 fine counts (≈ 65 keV)
4. Sweep coarse threshold while injecting 64 fine counts
 - Find threshold such that 50% chance of triggering
 - Set threshold slightly higher to avoid cancelling below baseline
5. Inject 3 coarse counts (=3 x 64 fine counts) and sweep coarse packet size
 - Find maximum packet corresponding to 2 coarse counts
6. Fine-tune coarse packet size by verifying goodness of fit while injecting increasing charge per frame



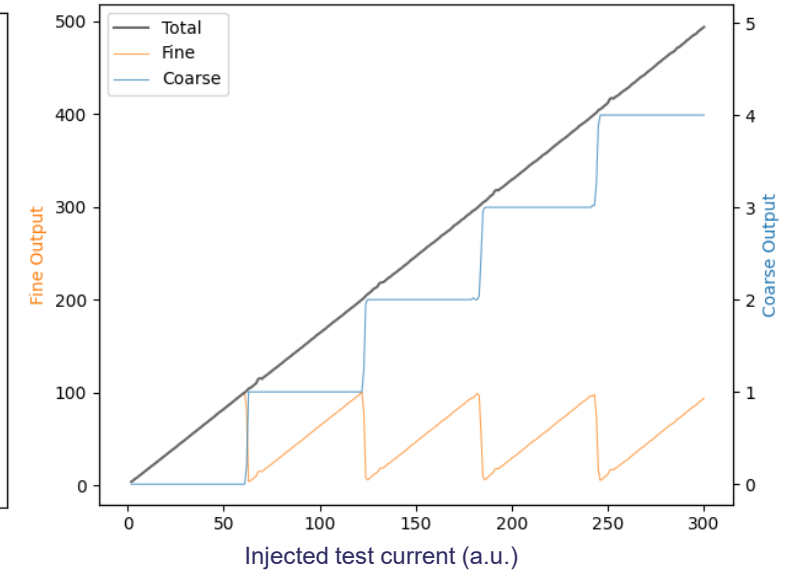
Pixel Test Results – Coarse Stage (inbuilt test pulse)



Sweep coarse stage threshold →
measure coarse output → repeat to
obtain probability of triggering →
determine dark noise



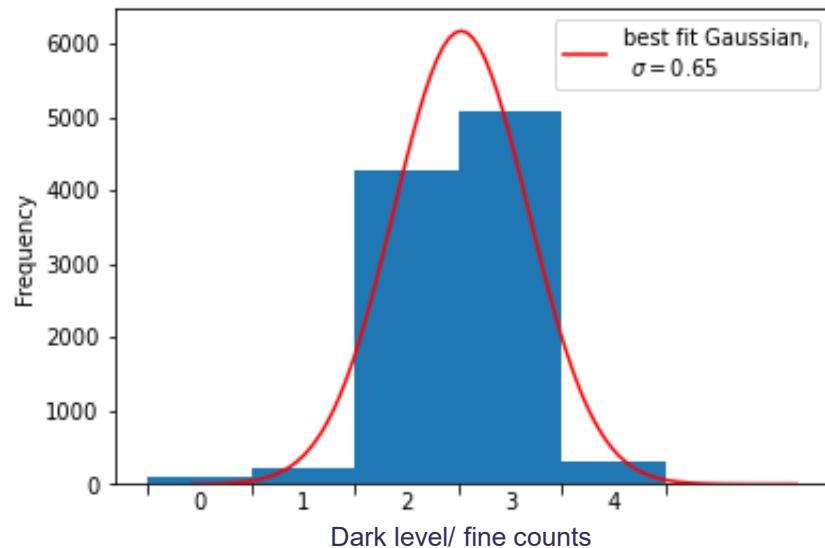
Vary input test current →
measure coarse ADC output



Vary input test current →
measure coarse and fine counts

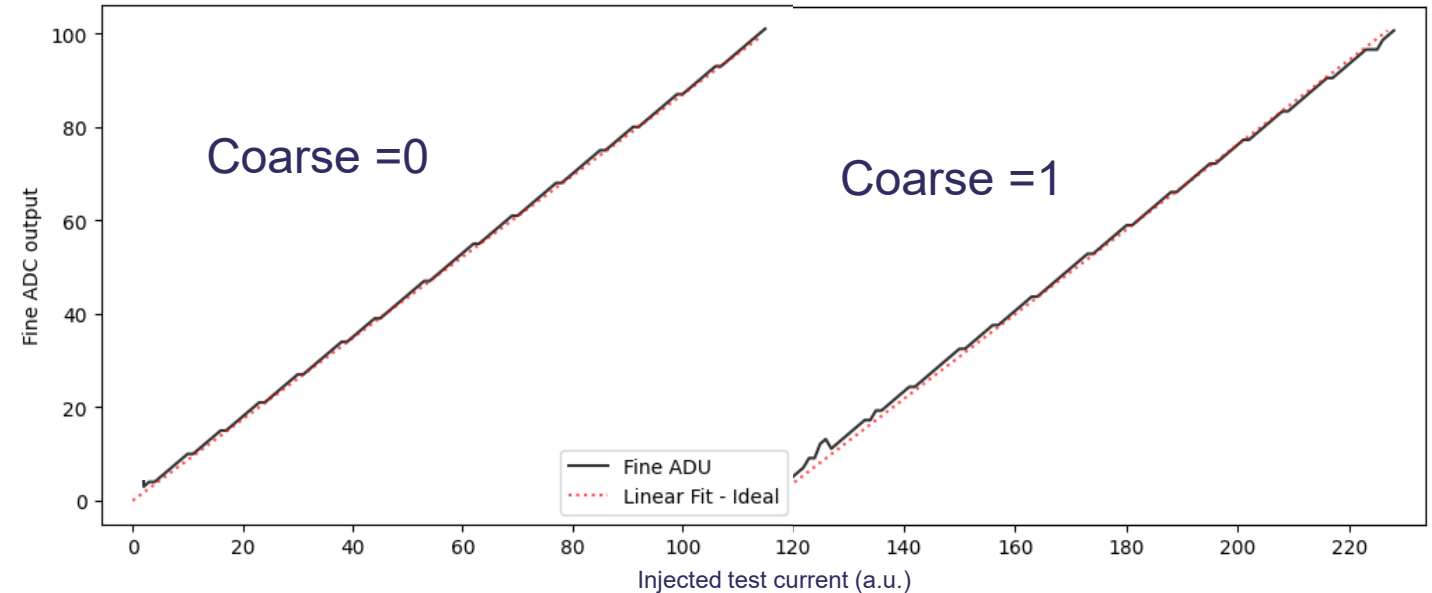
Pixel Test Results – Fine Stage (inbuilt test pulse)

1 fine count = 0.25 of 30keV Photon



No current injected → measure counts due to noise

Fine stage ADC linearity



Increase injected current → measure fine ADC output

Single photon peaks

- Monochromatic beam focused to sub-pixel level $\approx 50 \mu\text{m} \times 50 \mu\text{m}$ and centred in pixel
- Can resolve single photon peaks with a width $\sigma \approx 5.7\text{keV}$ ($\sim 1200 \text{ e}^-$ RMS)
- Misalignment between peaks of same energy:
 $\mu_{2 \times 20 \text{ keV}} \neq \mu_{40 \text{ keV}}$
 $\mu_{3 \times 20 \text{ keV}} \neq \mu_{2 \times 30 \text{ keV}}$
...
- Thought to be due to flux-dependent excess leakage current in HF-CZT

